

OpenMP-Parallel Discrete Adjoint in SU2 with OpDiLib

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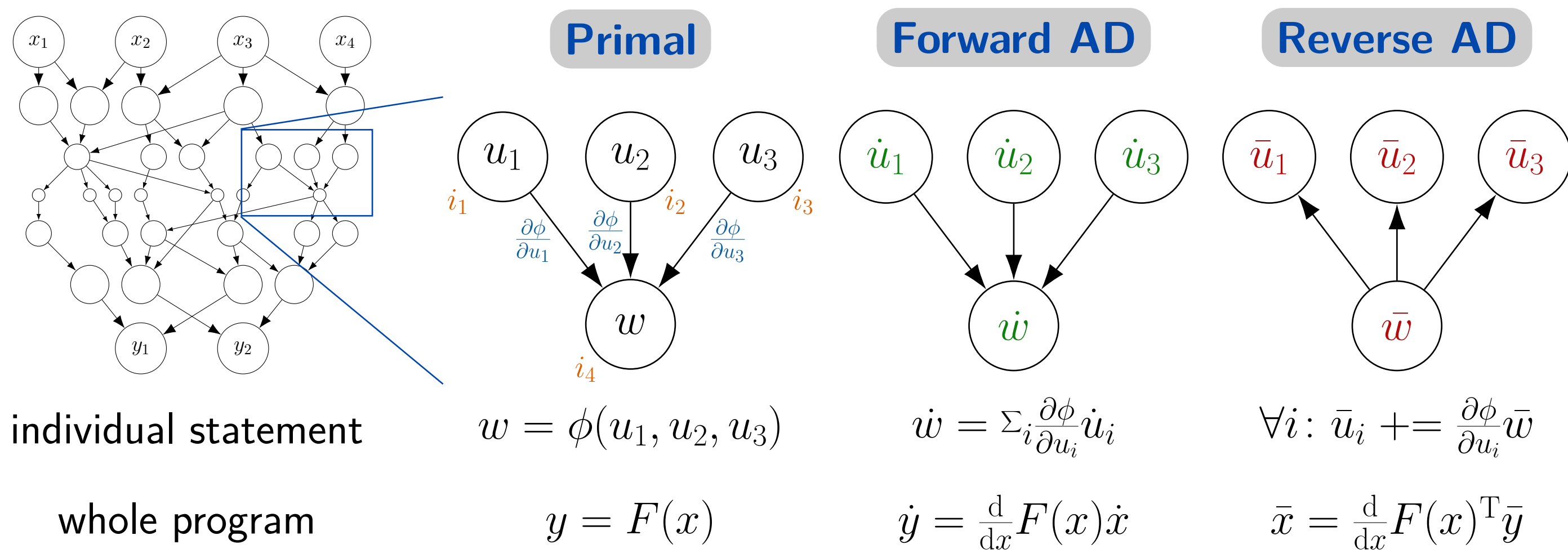
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Context and Goals

- open-source multiphysics simulation-suite **SU2** [7]
 - SU2 features **discrete adjoints** by means of operator-overloading (OO) **automatic differentiation** (AD) [1, 2]
 - both primal and discrete adjoint solvers support **MPI** parallelism
 - primal solvers support **OpenMP** parallelism [8]
- goal**: enable and improve OpenMP-parallel discrete adjoint solvers in SU2 [6, 4]
- apply **OpDiLib**, an add-on for OO-AD tools providing reverse AD of OpenMP [5, 3]



Automatic Differentiation



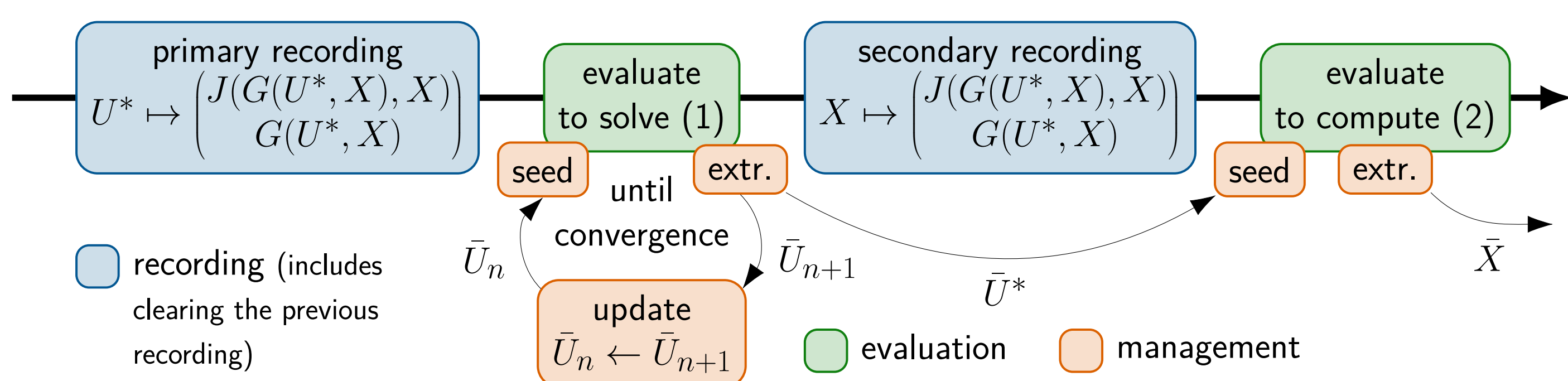
- computational graph with values as nodes and direct dependencies as edges
 - edges annotated with **partials**, nodes annotated with **identifiers** (virtual addresses)
 - identifiers address into memory for **tangent values** or **adjoint values**
- see [9] for AD in general, the above corresponds to Jacobian taping in CoDiPack [10]

Discrete Adjoint in SU2

- primal fixed-point equation $U = G(U, X)$ with parameters X , state U , and iterator G
- with primal solution U^* and objective function J , compute $\bar{X} = \frac{d}{dX} J(G(U^*, X), X)^T$ by means of discrete adjoint equations

$$\bar{U} = \frac{\partial}{\partial U} J(G(U^*, X), X)^T + \frac{\partial}{\partial U} G(U^*, X)^T \bar{U}, \quad (1)$$

$$\bar{X} = \frac{\partial}{\partial X} J(G(U^*, X), X)^T + \frac{\partial}{\partial X} G(U^*, X)^T \bar{U} \quad (2)$$



Enabling OpenMP-Parallel Discrete Adjoint

Coupling with OpDiLib

- enables **recording** of OpenMP-parallel code
- provides corresponding parallel **evaluation**
- `SU2_OMP_* / END_SU2_OMP_*` macros
 - conform with OpDiLib's macro backend
- pair recording positions with OpDiLib states
- parallel external function treatment of LSEs

```
SU2_OMP_PARALLEL
{
  SU2_OMP_FOR_(SU2_NOWAIT)
  for (int i = 0; i < n; ++i)
  { ... }
  END_SU2_OMP_FOR
  ...
  SU2_OMP_BARRIER
  ...
}
END_SU2_OMP_PARALLEL
```

Exchange Identifier Management

- previous linear management strategy (LMS) assumes sequential recording
 - copies share identifiers, like references for AD
- transition to a parallel reuse management strategy
 - rewrite code that depends on properties of LMS

Parallelized AD Workflow

- revised storage layout of input/output identifiers
- parallel **management**
- parallel (re)computation of dependent quantities

Performance Improvements

Adjoint Vector Management

- CoDiPack's vector of adjoint variables is a shared resource
- mutual exclusion of resizing and reading/writing by shared mutex
 - less exclusive locking: explicit resizing, access without bounds checking
 - less shared locking: single lock for multiple subsequent lock-free accesses

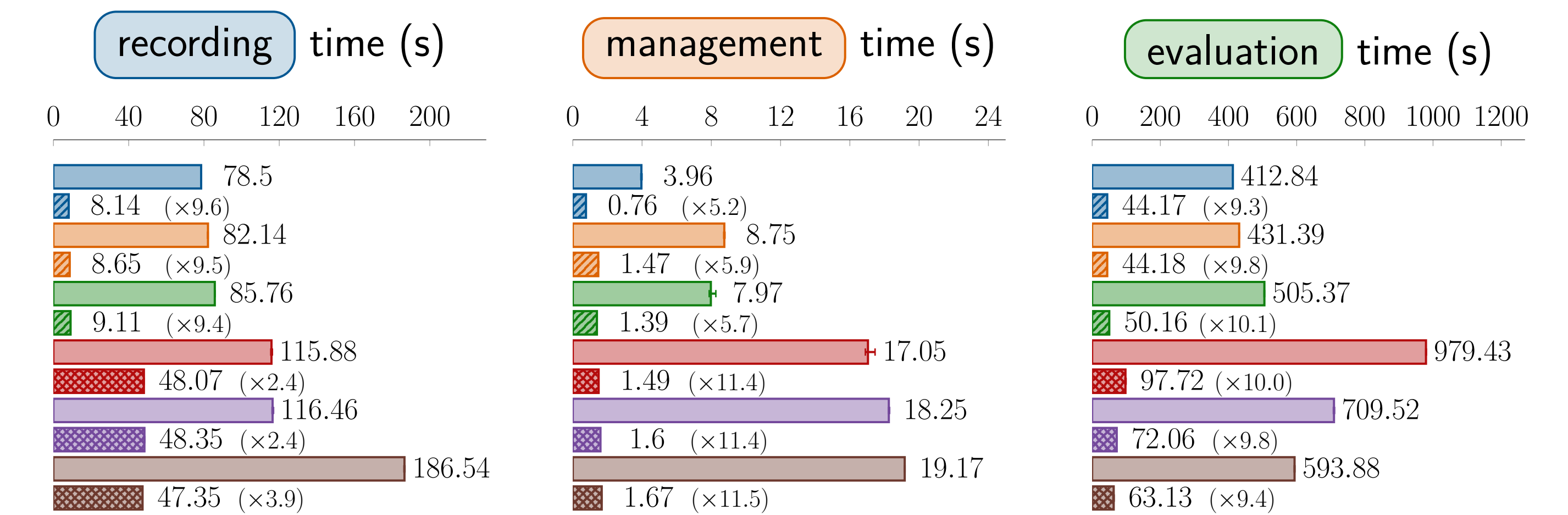
Shared Reading Optimization

- non-exclusive primal reads become non-exclusive adjoint writes
- data races resolved by atomic updates on adjoint variables
- leverage developer knowledge: annotate code parts with exclusive read access
 - evaluations of those parts do not use atomics

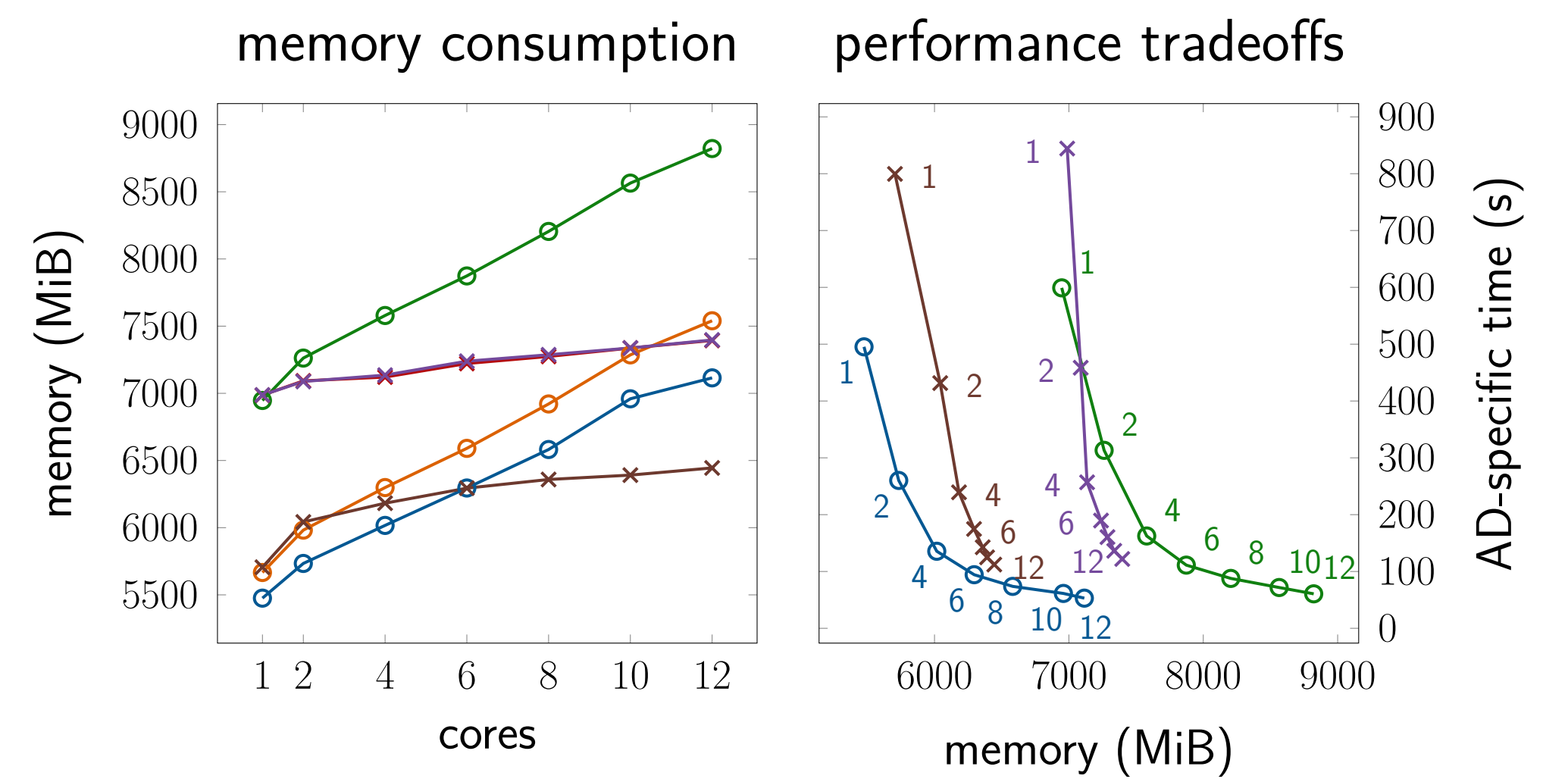
Preaccumulations with Shared Inputs

- preaccumulations: early contractions of computational subgraphs [9]
- simultaneous thread-local preaccumulations with shared inputs result in data races [6, 4]
- default: disable these preaccumulations
- transition to thread-local adjoint variables for re-enabling these preaccumulations [4]
 - here: thread-local adjoints via `std::map<int, double>`

Onera M6 Benchmark



- MPI build (linear)
 - 1p 12p 1-12p
- MPI build (reuse)
 - 1p 12p 1-12p
- MPI build (+partly disabled preacc.)
 - 1p 12p 1-12p
- OpenMP build (opt. adj. vec. mgmt.)
 - 1t 12t 1-12t
- OpenMP build (+shared read. opt.)
 - 1t 12t 1-12t
- OpenMP build (+preacc. with sharing mapped)
 - 1t 12t 1-12t



- primal solver: air flow around an Onera M6 wing
 - AoA 3.06°, Mach 0.8395, Re 11.72·10⁶, L 0.64607 m
- RANS-SA, JST, Green-Gauss, FGMRES, ILU
- aerodynamic lift as objective function
- mesh coordinates as parameters
- solve (1) by 300 GMRES iterations
 - restart Krylov solver every 50th iteration
- Skylake node of the Elwetritsch cluster at RPTU
 - Intel Xeon Gold 6126 (12 cores, 1 NUMA domain)

References

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SU2 logo courtesy: SU2 foundation, https://su2foundation.org/wp-content/uploads/2019/10/SU2_Logo_eps.zip (visited on Aug. 15, 2024)



Primary references. See also references cited therein.

